

L Number	Hits	Search Text	DB	Time stamp
-	634	703/14.ccls.	USPAT; US-PGPUB	2004/05/19 12:18
-	75	703/14.ccls. and (programmable adj logic adj device or pld or field adj programmable adj gate adj array or fpga)	USPAT; US-PGPUB	2004/05/19 12:19
-	0	guccione-steven.in.	USPAT; US-PGPUB	2004/05/19 14:46
-	35	guccione.in.	USPAT; US-PGPUB	2004/05/19 16:25
-	0	mcmillan-s.in.	USPAT; US-PGPUB	2004/05/19 16:21
-	3	"6510546"	USPAT; US-PGPUB	2004/05/20 07:55
-	519	716/11.ccls.	USPAT; US-PGPUB	2004/05/20 09:19
-	515	716/11.ccls. and (FPGA or field programmable gate array or PLD or programmable logic device)	USPAT; US-PGPUB	2004/05/20 07:56
-	30	(716/11.ccls. and (FPGA or field programmable gate array or PLD or programmable logic device)) and (reconfigurable or reconfiguration or configurable)	USPAT; US-PGPUB	2004/05/20 07:57
-	395	716/16.ccls.	USPAT; US-PGPUB	2004/05/20 09:19
-	236	716/16.ccls. and (fpga or field adj programmable adj gate adj array)	USPAT; US-PGPUB	2004/05/20 09:20
-	181	(716/16.ccls. and (fpga or field adj programmable adj gate adj array)) and (reconfigurable or configurable or reconfiguration)	USPAT; US-PGPUB	2004/05/20 09:20
-	54	((716/16.ccls. and (fpga or field adj programmable adj gate adj array)) and (reconfigurable or configurable or reconfiguration)) and simulation	USPAT; US-PGPUB	2004/05/20 09:20
-	35	guccione.in.	USPAT; US-PGPUB	2004/05/21 12:15
-	0	guccione.in. and queue	USPAT; US-PGPUB	2004/05/21 12:15
-	1	guccione.in. and event	USPAT; US-PGPUB	2004/05/21 12:16
-	87	event adj driven adj simulation	USPAT	2004/05/21 12:17
-	3456	(event adj driven adj simulation) and FPGA or PLD	USPAT	2004/05/21 12:16
-	14	(event adj driven adj simulation) and (FPGA or PLD)	USPAT	2004/05/21 12:26
-	12	((event adj driven adj simulation) and (FPGA or PLD)) and (queue or LIFO or asynchronous or synchronous)	USPAT	2004/05/21 13:05